

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Applicant: Visokay et al.
Filed: Herewith
For: MOS TRANSISTOR GATES WITH DOPED SILICIDE AND
METHODS FOR MAKING THE SAME

INFORMATION DISCLOSURE STATEMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

1. Pursuant to 37 C.F.R. 1.97 and 1.98, and in compliance with 37 C.F.R. 1.56, the Office's attention is directed to the patents, pending applications, publications and other information listed on the attached PTO-1449. A copy of each listed document is enclosed except for: (a) pending applications or (b) those previously cited or submitted to the Office in the following application(s) upon which this application relies for an earlier filing date under 35 U.S.C. 120:

Serial No.: _____
Filing Date: _____

Regarding any document, publication or other information for which a date is not given on the attached PTO-1449, Applicant(s) believe(s) the same may qualify as "prior" art to this application and should be treated accordingly, although Applicant(s) reserve(s) the right to contest the prior art status of any document, publication or information, should issue arise.

2. Regarding each listed document that is not in the English language, an English-language translation accompanies this Statement as indicated on the attached PTO-1449 or a concise explanation of the relevance of the document is set forth in the following document(s):

- (a) ____ Copy of each English language version of a search report indicating the degree of relevance found by the foreign office of each document being submitted from the search report.
- (b) ____ Attachment entitled "Concise Explanation of Relevance of Non-English Language Documents".

3. Pursuant to 37 C.F.R. 1.97(b) this Statement is being filed (one must be checked):

- (a) X Within 3 months of the filing date or date of entry into the National Stage.
- (b) ____ Before the mailing date of a first Office Action on the merits. If this Statement is not filed before the mailing date of a first Office Action on the merits, the required certification is given below or, in the absence thereof, the Office is authorized to charge the required fee set forth in 37 C.F.R. 1.17(p) to Deposit Account No. 20-0668 for consideration of this Statement.
- (c) ____ Before the mailing date of a first Office Action on the merits after a first or second submission under 37 C.F.R. 1.129(a).

(d) ___ After the period set forth in 37 C.F.R. 1.97(b) but before the mailing date of either a final action or a notice of allowance.

(1) ___ The required certification is given below, or

(2) ___ Enclosed is a check covering the fee set forth in 37 C.F.R. 1.17(p) for consideration of this Statement, or

(3) ___ Charge the fee set forth in 37 C.F.R. 1.17(p) to Deposit Account No. 20-0668

(e) ___ After the mailing date of either a final action or a notice of allowance, but before payment of the issue fee. Petition hereby is made for consideration of this Statement and the required certification is indicated below.

(1) ___ Enclosed is a check covering the fee set forth in 37 C.F.R. 1.17(i)(1), or

(2) ___ Charge the fee set forth in 37 C.F.R. 1.17(i)(1) to Deposit Account No. 20-0668.

4. Certification (if applicable)

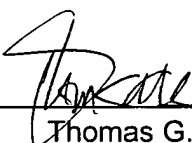
(a) ___ The undersigned hereby certifies that each item of information contained in this Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than 3 months prior to the filing of this Statement.

(b) ___ The undersigned hereby certifies that no item of information contained in this Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the undersigned's knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. 1.56(c) more than 3 months prior to the filing of this Statement.

5. The Commissioner is hereby authorized to charge any additional fees or credit any overpayment to Deposit Account No. 20-0668.

Respectfully Submitted,

ESCHWEILER & ASSOCIATES, LLC

By  _____
Thomas G. Eschweiler
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Cleveland, Ohio 44114
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CERTIFICATION UNDER 37 CFR 1.10

I hereby certify that this paper, and the documents referred to as attached or enclosed, are being deposited with the United States Postal Service on the date set forth below in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR 1.10, with the below indicated mailing label number, addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: September 30, 2003


Christine Gillroy

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PTO/SB/08A (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete If Known	
				Application Number	
				Filing Date	
				First Named Inventor Visokay et al.	
				Group Art Unit	
Examiner Name					
Sheet	1	of	2	Attorney Docket No. TI-35943	

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	AA	4,628,588		McDavid	12/16/1986	Entire Document
	AB	4,641,417		McDavid	02/10/1987	Entire Document
	AC	4,672,419		McDavid	06/09/1987	Entire Document
	AD	4,954,867		Hosaka	09/04/1990	Entire Document
	AE	5,633,522		Dorleans et al.	05/27/1997	Entire Document
	AF	5,723,893		Yu et al.	03/03/1998	Entire Document
	AG	5,937,315		Xiang et al.	08/10/1999	Entire Document
	AH	6,376,342	B1	Tseng	04/23/2002	Entire Document
	AI	6,410,967	B1	Hause et al.	06/25/2002	Entire Document
	AJ	6,432,817	B1	Bertrand et al.	08/13/2002	Entire Document
	AK	6,602,781	B1	Xiang et al.	08/05/2003	Entire Document
	AL	6,617,624	B2	Powell	09/09/2003	Entire Document

FOREIGN PATENT DOCUMENTS								
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		Office ³	Number ⁴	Kind Code ² (if known)				
	BA							
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
	CA	“Mo₂N/Mo GAFP MOSFETs”, MANJIN J. KIM and DALE M. BROWN, IEEE, 1982, pp. 560-563.		
	CB	“Application of MoSi₂ to the Double-Level Interconnections of I²L Circuits”, YOSHITAKA SASAKI, OSAMU OZAWA and SHUICHI KAMEYAMA, IEEE Transactions on Electron Devices, Vol. ED-27, No. 8, August, 1989, 5 pgs.		
	CC	“Work Function Controlled Silicide Technology for Submicron CMOS”, MASAKAZU KAKUMU and KAZUHIKO HASHIMOTO, Source and date unknown, 2 pgs.		
	CD	“REFRACTORY METAL SILICIDE/N+ POLYSILICON IN CMOS/SOS”, B. C. LEUNG and J. S. MAA, Source and date unknown, 4 pgs.		

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

US and Foreign Patent Documents: ¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

Other Prior Art/Non-Patent Literature Documents: ¹Unique citation designation number. ²Applicant is to place a check mark here if English Translation is attached.

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Examiner Name					
Sheet	2	of	2	Attorney Docket No.	TI-35943

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CE	"Fabrication of Mo-Gate/TI-Silicide-Clad-Moat MOS Devices by Use of Multilayer-Glass Depositions", J. M. MCDAVID, IEEE Electron Device Letters, Vol. EDL-5, No. 9, September, 1984, 2 pgs.	
	CF	"Lightly Impurity Doped (LD) Mo Silicide Gate Technology", MASAKAZU KAKUMU and JUN'ICHI MATSUNAGA, IEEE, 1985, pp. 415-418.	
	CG	"An Investigation of Molybdenum Gate for Submicrometer CMOS", ROBERT F. KWASNICK, EDMUND B. KAMINSKY, PAUL A. FRANK, GERHARD A. FRANZ, KENNETH J. POLASKO, RICHARD J. SAIA and THOMAS B. GORCZYA, IEEE Transactions on Electron Devices, Vol. 35, No. 9, September, 1988, 7 pgs.	
	CH	"Electrical Characteristics of TiB ₂ for ULSI Applications", CHANG SIK CHOI, QINGFENG WANG, CARLTON M. OSBURN, GARY A. RUGGLES and AYAN S. SHAH, IEEE Transactions on Electron Devices, Vol. 39, No. 10, October, 1992, pp. 2341-2345.	
	CI	"Fin FET Process Refinements for Improved Mobility and Gate Work Function Engineering", YANG-KYU CHOI, LELAND CHANG, PUSHKAR RANADE, JEONG-SOO LEE, DAEWON HA, SRIRAM BALASUBRAMANIAN, ADITYA AGARWAL, MIKE AMEEN, TSU-JAE KING, and JEFFREY BOKOR, IEEE, 2002, 4 pgs.	
	CJ	"Silicon Processing for The VLSI Era, Volume 2: Process Integration", MOS DEVICES AND NMOS PROCESS INTEGRATION, STANLEY WOLF PhD. 1990, 7 PGS.	
	CK		
	CL		
	CM		
	CN		

Examiner Signature		Date Considered	
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